

EMBEDDED SYSTEM DEV.

CODE: (EMBD-2M)

1. Embedded System Development

- a. HARVARD /SUPER HARVARD ARCH/ VON NEUMANN
- b. TYPES OF MICRO-CONTROLLERS
- c. RISC VS CISC
- d. MEMORY HIERARCHY
- e. 8 BIT MICROCONTROLLERS

2. INTEL 8051

- a. ARCHITECTURE
- b. INSTRUCTION SET, PIN DIAGRAM
- c. MEMORY (RAM, ROM), STACK
- d. SFR'S, FLAGS, ADDRESSING MODES
- e. TIMERS, CLOCKS
- f. INTERRUPTS, ISR
- g. INTERFACING WITH KEYBOARD, STEPPER MOTOR, ADC/DAC,LCD
- h. 7-SEGMENT DISPLAY, RTC, RELAYS, SERIAL COMMUNICATION

3. KEIL (DEVELOPMENT TOOL)

- a. DISCUSSION ON DIFFERENT MEMORY MODULES
- b. POINTERS & EXTENSIONS IN C51, ABSOLUTE VARIABLE DECLARATIONS
- c. STORAGE CLASSES & ITS IMPORTANCE IN EMBEDDED SYSTEMS
- d. FUNCTIONS AND EXTENSIONS IN C51, DISCUSSION ON
- e. RE-ENTRANCY, INTERRUPTS & IMPORTANCE OF GLOBAL VARIABLES
- f. USAGE OF BIT WISE OPERATORS IN EMBEDDED SYSTEMS

4. EMBEDDED C

- a. INTRODUCTION TO EMBEDDED C
- b. COMPILATION PROCESS, DESIGN APPROACH
- c. DATA TYPES, VARIABLES, LOOPS, NESTED LOOPS
- d. FUNCTIONS, ARRAYS
- e. STRUCTURE, UNIONS, TYPEDEF, ENUMS
- f. POINTERS, STRING OPERATION
- g. WORKING ON BITS MANIPULATIONS
- h. ADVANCED USE OF THE PREPROCESSOR

✚ LINKED LIST